

Amendments to the Specification:

Please replace the paragraph [0007] beginning at page 2 with the following amended paragraph:

Figure 1 is a block diagram illustrating a pipelined programmable processor 100, which includes a future file 102 206 for storing speculative data address values according to an embodiment. The processor 100 may include an execution pipeline 102 and a control unit 104. The control unit 104 may control the flow of instructions and data through the pipeline 102 in accordance with a system clock 106. During the processing of an instruction, the control unit 104 may direct the various components of the pipeline to decode the instruction and to perform the corresponding operation including, for example, writing results back to memory or registers.